



(19) Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) Publication number:

0 405 695 A1

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 90201739.1

(51) Int. Cl.⁵: G06K 7/00

(22) Date of filing: 29.06.90

(30) Priority: 30.06.89 NL 8901659

(43) Date of publication of application:
02.01.91 Bulletin 91/01

(84) Designated Contracting States:
AT BE CH DE FR GB IT LI NL

(71) Applicant: N.V. Nederlandsche
Apparatenfabriek NEDAP
Oude Winterswijkseweg 7
NL-7141 DE Groenlo(NL)

(72) Inventor: Stekelenburg, Jan Cornelis
Vlakkeweg 6
NL-7051 CH Varsseveld(NL)
Inventor: Tervoert, Marius Lambertus
Korte Molenstraat 11
NL-7271 BN Borculo(NL)
Inventor: Venema, Willem Hendrik Jacob
Calslaan 56 B 41
NL-7522 MG Enschede(NL)
Inventor: Scholten, Arjan Herbert
Ypkemeulestraat 33
NL-7543 CK Enschede(NL)

(74) Representative: Smulders, Theodorus A.H.J.,
Ir. et al
Vereenigde Octrooibureaux Nieuwe Parklaan
107
NL-2587 BP 's-Gravenhage(NL)

(54) An electromagnetic identification system for identifying a plurality of coded responders simultaneously present in an interrogation field.

(57) An electromagnetic detection and identification system for detecting and identifying a plurality of responders, each containing a unique code, and simultaneously present in one and the same interrogation field generated by a transmitter/receiver. According to the invention the transmitter/receiver is arranged to adjust the frequency of the interrogation field periodically to one of a pre-determined number of pre-determined values, and each responder is arranged to detect the instantaneous frequency of the interrogation field, the arrangement being such that, in a number of selection cycles, each time a

number of responders temporarily deactivate themselves in dependence upon the instantaneous frequency until one single responder remains which can generate the appurtenant code without becoming deactivated until the responder has been identified by the transmitter/receiver, whereafter the responder concerned is blocked by a change in field frequency, and the temporarily deactivated responders are reactivated, and the selection procedure is repeated until all responders in the interrogation field have been identified and blocked.

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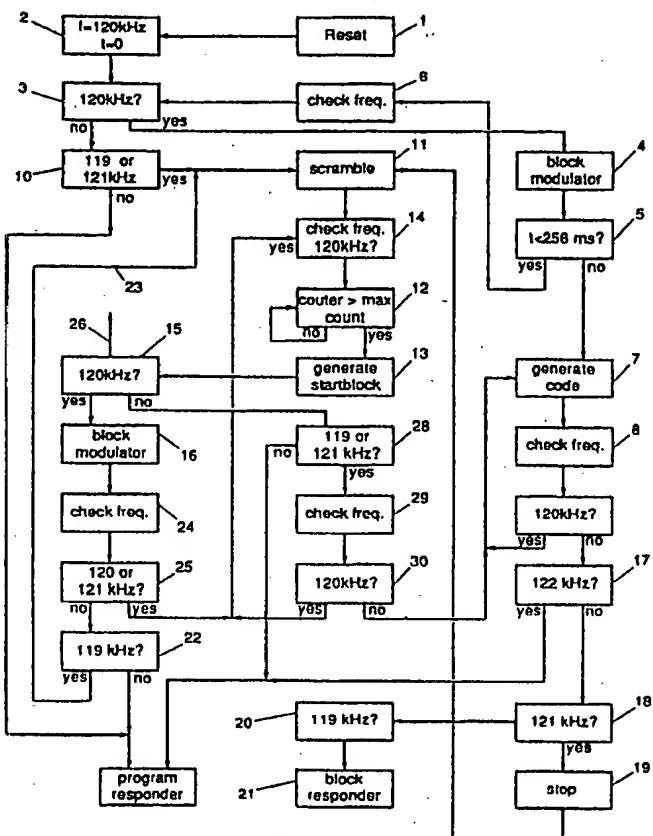


FIG. 1

AN ELECTROMAGNETIC IDENTIFICATION SYSTEM FOR IDENTIFYING A PLURALITY OF CODED RESPONDERS SIMULTANEOUSLY PRESENT IN AN INTERROGATION FIELD.

This invention relates to an electromagnetic detection and identification system for detecting and identifying a plurality of responders, each containing a unique code, and simultaneously present in one and the same interrogation field generated by a transmitter/receiver.

Electromagnetic detection/identification systems for human beings, animals and goods are at present used for countless applications. Such systems are used, *inter alia*, for permitting authorized persons access to, for example, protected premises. In such a system, one or more transmitters/receivers are disposed at the entrances and exits of a building, which are capable of recognizing a detection label or responder in the interrogation field. Thereafter the system can verify whether the person carrying the responder is authorized to enter or even leave the building or part of a building at that time. Electromagnetic detection systems are also used for the identification of articles. The recognition of articles may be of interest for the control of logistic processes in industrial enterprises. For example, these systems are used in the automobile industry for recognizing containers with specific parts and guiding these to the desired production site. These detection and identification systems can be so arranged that the responders do not require a power source of their own, but on the contrary, obtain their supply voltage from the interrogation field.

The major disadvantage of presently known systems is that correct identification is only possible if only one responder is present in the interrogation field. If two or more responders are present in the interrogation field, the unique codes generated by the responders are distorted, as a result of which the validation of the distorted codes leads to error detection in the transmitter/receiver. The result is that none of the responders can be detected. The condition that only one single responder may be present in the interrogation field presents problems for a considerable number of applications of detection systems, which necessitate adaptations which ensure that only one responder is present in the interrogation field at any time.

A method of identifying a plurality of responders in one interrogation field is disclosed in European patent application 0285419 and in applicant's Netherlands patent application 88,02718. That method involves an algorithmic selection procedure which uses a signal sent along with the interrogation carrier, which signal causes only one or a group of the responders that are to be iden-

tified to answer back with their code(s), and temporarily deactivates the other responders. In this arrangement, a strict sequence is applied for the identification. The disadvantage of this principle is that the operation is highly complex, so that the responder circuit is in practice relatively large, which has an adverse effect on current consumption. Furthermore, identifying responders one by one requires a great deal of communication between the transmitter/receiver and the responders. Moreover, this makes the cycle for the identification of the various responders relatively long.

It is an object of the present invention to remedy the drawbacks and disadvantages outlined above. More in general it is an object of the present invention to provide an effective and reliable electromagnetic detection/identification system which is capable of correctly and rapidly identifying a plurality of responders simultaneously present in an interrogation field.

For this purpose, according to the present invention, an electromagnetic identification system of the above kind is characterized in that said transmitter/receiver is arranged to adjust the frequency of the interrogation field periodically to one of a pre-determined number of pre-determined values, and each responder is arranged to detect the instantaneous frequency of the interrogation field, the arrangement being such that, in a number of selection cycles, each time a number of responders temporarily deactivate themselves in dependence upon the instantaneous frequency until one single responder remains which can generate the appurtenant code without becoming deactivated until the responder has been identified by the transmitter/receiver, whereafter the responder concerned is blocked by a change in field frequency, and the temporarily deactivated responders are reactivated, and the selection procedure is repeated until all responders in the interrogation field have been identified and blocked.

The invention will now be described in more detail hereinafter with reference to the accompanying drawings. In said drawings,

Fig. 1 shows schematically, by means of a flow chart, the operation of one embodiment of an identification system according to the present invention; and

Fig. 2 shows schematically one embodiment of a responder for a system according to the present invention.

Referring first to Fig. 1, when a responder is introduced into an interrogation field, it will bring itself into a starting position owing to the generation

of supply voltage from the interrogation field itself. The way in which a responder can take supply energy from an interrogation field is described, for example, in US patent 4,196,418, which is incorporated herein by reference. The circuit in the responder is in that case reset (block 1). The responder will then test for a pre-determined period of time, e.g. 256 ms, whether the frequency of the interrogation field is varied in this period (blocks 2-6). In the description of this invention it will be assumed, for the sake of simplicity and by way of example, that the frequency of the interrogation field may be 119, 120 or 121 kHz. Accordingly, the frequency will vary between these values. When the frequency continuously maintains a first value, e.g. 120 kHz, within the period of 256 ms, it is then assumed that the responder is present in the field of an electromagnetic detection system which does not operate in accordance with the present invention. After this period, the responder will be able to function as a responder as described in US patent 4,196,418, and will therefore start to modulate the interrogation field in a manner corresponding with the code of the responder, until the responder is removed from the field (block 7). In this situation, the responder is continuously switched in the active mode. However, the responder also continues to measure the frequency of the interrogation field (blocks 8,9). When the frequency changes, the active mode of the responder will yet be interrupted, in a manner to be described hereinafter, and the responder will then begin to participate in a selection procedure until recognition has taken place.

If, on the other hand, the responder has detected a variation of the frequency of the interrogation field within the period of 256 ms, e.g., from 120 kHz to 119 kHz or to 121 kHz (block 10) a selection procedure is started at once.

The selection procedure starts with the so-called scrambling (block 11) of all responders then present within the interrogation field. This is achieved by virtue of each responder being provided with a random number ranging between two values, e.g. 0 and 127, by means of a pseudo-random generator, built up, for example, around a shift register. This number serves as a starting value for a counter. Preferably, this is a counter already incorporated in the responder for the formation of the code. If desired, the random number can be formed by the pseudo-random generator in cooperation with the unique code of the relevant responder. Subsequently, the counter begins to count further from the random number. Furthermore, the random number may either be generated again each time, or be a pre-determined, fixed number for each responder. When each responder has its own pseudo-random generator, the gener-

ator may be built up, for example, from a shift register which, for the calculation of the random number ranging, for example, between 0 and 127, is each time loaded with a number of bits obtained by continually comparing a varying part of the code with the bits then present in the shift register. Thereafter the bits in the shift register are shifted by one position, so that after a cycle a random number in binary form is again available. The responder whose counter first reaches the transition from value 127 to value 0 (block 12) is capable of transmitting a so-called starting block. This starting block may, for example, be a load on the interrogation field which is equal to the load caused by a binary number of eight bits each having the value zero. In the mean time, the frequency is also controlled (block 14). When the transmitter/receiver has recognized the starting block, the field frequency of 119 or 121 kHz initially detected by the responder is again changed into a frequency of 120 kHz. All responders which detect the change in frequency (block 15), and whose counters have not yet reached the transition from value 127 to value 0 are switched into a passive mode (block 16) by the frequency change. The greater part of the responders present in the interrogation field are thus temporarily left out of account for the identification. The other responders, i.e., whose counters have passed the final position of 127, will be going to generate their unique code in known manner by modulating the interrogation field. If more than one responder transmit a code in this manner, the receiver will report an error. This can be effected, for example, by changing the field frequency from 120 kHz to 121 kHz. For these responders this is the indication that a selection procedure must again be followed. The selection procedure is repeated in this way until finally one responder remains. This responder is enabled to transmit its unique code completely by rhythmically loading the interrogation field (arrow 26). During this time, all other responders wait in the passive mode (block 16) until the selected responder has been identified. As soon as the selected responder has been recognized, the transmitter again changes its frequency, for example, to 119 kHz. When the responder selected and identified detects the new frequency, it is switched into a passive mode, i.e., it is excluded from participation in any subsequent selection procedure (blocks 20,21). This passive mode can only be changed when the circuit in the responder is reset, as is the case, for example, when the responder has first been removed from, and then reintroduced into, the interrogation field. For the responders which are in the passive mode, the change in interrogation field frequency from 120 to 119 kHz is an indication to re-participate into the selection procedure according to the invention

(block 22, arrow 23). As in the preceding selection procedure, one, but then a different, responder will remain. These cycles will be repeated until the last responder has been recognized. An important feature is that, in each cycle, a relatively high percentage of responders is excluded from selection (blocks 16,24,25). As a result, the time needed for reading the codes of all responders present in the interrogation field is relatively short.

For the purposes of the present invention, the detection system should be so arranged that there can be data transmission between the transmitter/receiver and responder as well as from the responder to the transmitter/receiver. With regard to the transmission of data from responder to transmitter/receiver, the invention is based on the well-known principle that the responder obtains supply voltage for the responder circuits from the interrogation field, and that the interrogation field is also used for the transmission of data between responder and transmitter/receiver. Naturally, each responder may have its own power source in this system, for example, in the form of a battery. Moreover, it is not necessary for the data transmission from responder to transmitter/receiver to be effected through the interrogation field. For example, each responder may be equipped with a transmission device which uses a different frequency for the transmission of data from the frequency of the interrogation field of the transmitter/receiver.

If desired, the responder circuit may include a programming mode in addition to a reading mode. When, by means of the selection according to the invention, for example, one responder remains, such responder may be switched into a programming mode by a pre-determined variation of the frequency of the interrogation field. This could be effected, for example, by adjusting the field frequency to 122 kHz (block 17). Thereafter the appropriate data in the memory can be changed (block 27), and the programming mode can again be left by changing the frequency.

Fig. 2 shows schematically an example of a responder 40 suitable for use in an electromagnetic identification system according to the present invention. As shown, the responder comprises a resonant circuit 41 with a coil L and a capacitor C. The resonant circuit is connected to a block 42 which in known manner, for example as described in US patent 4,196,418, provides supply voltage Vdd and clock signals CL, and which further comprises a switching means for varying the electrical properties of the resonant circuit in accordance with the code of the relevant responder. The clock signals are derived from the A.C. voltage generated across the resonant circuit by the interrogation field, and consequently form a measure for the frequency of the interrogation field. The clock sig-

nals are supplied to a frequency counter 43 and to the control logic 44 of the responder. The frequency counter is connected to a crystal oscillator 45 with a crystal 46, serving as a reference.

5 As indicated by a frame 47, the responder circuit may take the form of an integrated circuit, to which the crystal and the LC circuit require to be connected as the only external elements.

10 The frequency counter 43 has various outputs 48,49,50,51, which each correspond to one of the field frequencies to be measured. Thus output 48 provides a signal if a field frequency of 119 kHz has been detected; output 49 if a field frequency of 120 kHz has been detected, etc. These signals are 15 supplied to the control logic 44 and to a memory 52, comprising the code, which for example may be an EEPROM.

20 The control logic transmits clock pulses CL to scrambler 53 and an address counter 54. These 25 may be the same clock pulses as supplied by block 42 to the control logic, or pulses derived therefrom. The control logic further transmits control signals to scrambler 53, memory 52 and address counter 54, as indicated by lines 55,56 and 30 57. As soon as the counter coupled to scrambler 53, which may form part of the scrambler, as indicated at 58, or of the control logic, as indicated in dash lines at 58, passes the final position 127, the control logic effects, for example as indicated through address counter 54, memory 52 and decoder 60, that the starting block is transmitted. For this purpose, the scrambler is connected to the control logic. If desired, the address counter may 35 serve also as the counter for the scrambler. The decoder has an output MOD, which is connected to the switching means in block 42.

40 Accordingly, the only purpose of the scrambler is to provide a randomly chosen waiting period for each responder before it can transmit a starting 45 block. As soon as the first responder has transmitted a starting block, all other responders whose counters have not yet passed the final position are switched into a passive mode. This is possible because of the transmitter/receiver responding to the correct reception of a starting block by changing the field frequency, for example, to 120 kHz.

50 A change of the field frequency is detected through the frequency counter by the control logic, which subsequently generates the appropriate control signals.

55 Upon a change in field frequency from 119 kHz or 121 kHz to 120 kHz, the control logic blocks, for example, through lines 56 and 57 the possibility of transmitting the responder code, except in case the counter 58 or 58 has then already passed the final position. Such blocking may alternatively be realized in a different way. Thus, it is possible for the control logic to directly block the switching means

in block 42.

If the situation should occur that, after the moment when the field frequency has been adjusted to 120 kHz, two or more responders transmit their code, because their counters 58 or 58' had passed the final position in good time, the transmitter/receiver detects an error, and the field frequency is again changed, e.g. to 121 kHz. Subsequently the responders which simultaneously generate a code signal are involved in a continued selection procedure (blocks 17-19 of Fig. 1).

If, however, the field frequency is not changed to 121 kHz, this means that only one single responder generates a code signal, which can then be fully generated. When the responder thus selected has been identified, the transmitter/receiver again changes the field frequency to, for example, 119 kHz, as described before, and the responders switched to the passive mode in the preceding procedure are involved in a new selection procedure.

As shown, there is a line 51 between the frequency counter 43 and memory 52. Line 51 serves for switching the memory to the programming mode after the associated field frequency (122 kHz) has been detected.

It is observed that, after reading the above, various modifications will readily occur to those skilled in the art. Thus, the frequency values have been mentioned by way of example only. Also, the construction of the responders may vary. This is partly dependent upon the purpose for which the responders are used. These and similar modifications are considered to fall within the scope of the present invention.

Claims

1. An electromagnetic detection and identification system for detecting and identifying a plurality of responders, each containing a unique code, and simultaneously present in one and the same interrogation field generated by a transmitter/receiver, characterized in that said transmitter/receiver is arranged to adjust the frequency of the interrogation field periodically to one of a pre-determined number of pre-determined values, and each responder is arranged to detect the instantaneous frequency of the interrogation field, the arrangement being such that, in a number of selection cycles, each time a number of responders temporarily deactivate themselves in dependence upon the instantaneous frequency until one single responder remains which can generate the appurtenant code without becoming deactivated until the responder has been identified by the transmitter/receiver, whereafter the responder concerned is blocked by

a change in field frequency, and the temporarily deactivated responders are reactivated, and the selection procedure is repeated until all responders in the interrogation field have been identified and blocked.

- 5 2. An electromagnetic detection and identification system as claimed in claim 1, characterized in that each responder comprises a counter which after the detection of a pre-determined field frequency begins to count from a random number, and when the responder reaches a pre-determined counter position, which is the same for all responders, it generates a starting code which results in such a change in frequency of the interrogation field as to temporarily deactivate all responders which have not yet reached said pre-determined counter position.
- 10 3. An electromagnetic detection and identification system as claimed in claim 2, characterized in that each responder comprises a pseudo-random generator which each time determines the random number.
- 15 4. An electromagnetic detection and identification system as claimed in claim 3, characterized in that the pseudo-random generator for each responder is built up from a shift register which to calculate the random number is periodically loaded with a number of bits obtained by comparing each time a different part of the unique code with the bits then present in the shift register, whereafter the bits are shifted by one position in the shift register, so that a new random number in binary form becomes available for the responder each time a cycle is terminated.
- 20 5. An electromagnetic detection and identification system as claimed in any one of claims 1-4, characterized in that each responder comprises a control logic circuit which upon detection of a pre-determined change in field frequency terminates the temporarily deactivated condition.
- 25 6. An electromagnetic detection and identification system as claimed in any of the preceding claims, characterized in that the transmitter/receiver and responder are so arranged as to permit simultaneous data transmission from transmitter/receiver to responder and from responder to transmitter/receiver, the data transmission from transmitter/receiver to responder being effected by variation in the frequency of the interrogation field, and the data transmission from the responder to the transmitter/receiver being effected by varying absorptions of the interrogation field.
- 30 7. An electromagnetic detection and identification system as claimed in any of the preceding claims, characterized in that the responders are so arranged that a pre-determined change in frequency of the interrogation field will bring one or more responders into a programming mode, so that the
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responder memory, in which the unique code is stored, or a part thereof, can be changed.

8. An electromagnetic detection and identification system as claimed in claim 1, characterized in that all responders are brought into a starting position relevant to the selection upon the generation of supply voltage in the responder as a result of the responder being introduced into the interrogation field generated by the transmitter/receiver.

9. An electromagnetic detection and identification system as claimed in any of the preceding claims, characterized in that each responder is provided with a power source of its own, such as a battery, for the transmission of data from the responder to the transmitter/receiver.

10. An electromagnetic detection and identification system as claimed in any of the preceding claims, characterized in that each responder is provided with a transmitting device capable of transmitting data from the responder to the transmitter/receiver by modulating a frequency not equal to the frequency generated by the transmitter/receiver.

11. An electromagnetic detection and identification system as claimed in any of the preceding claims, characterized in that each responder comprises a frequency counter.

12. An electromagnetic detection and identification system as claimed in claim 11, characterized in that the frequency counter is coupled with a crystal oscillator serving as a frequency reference.

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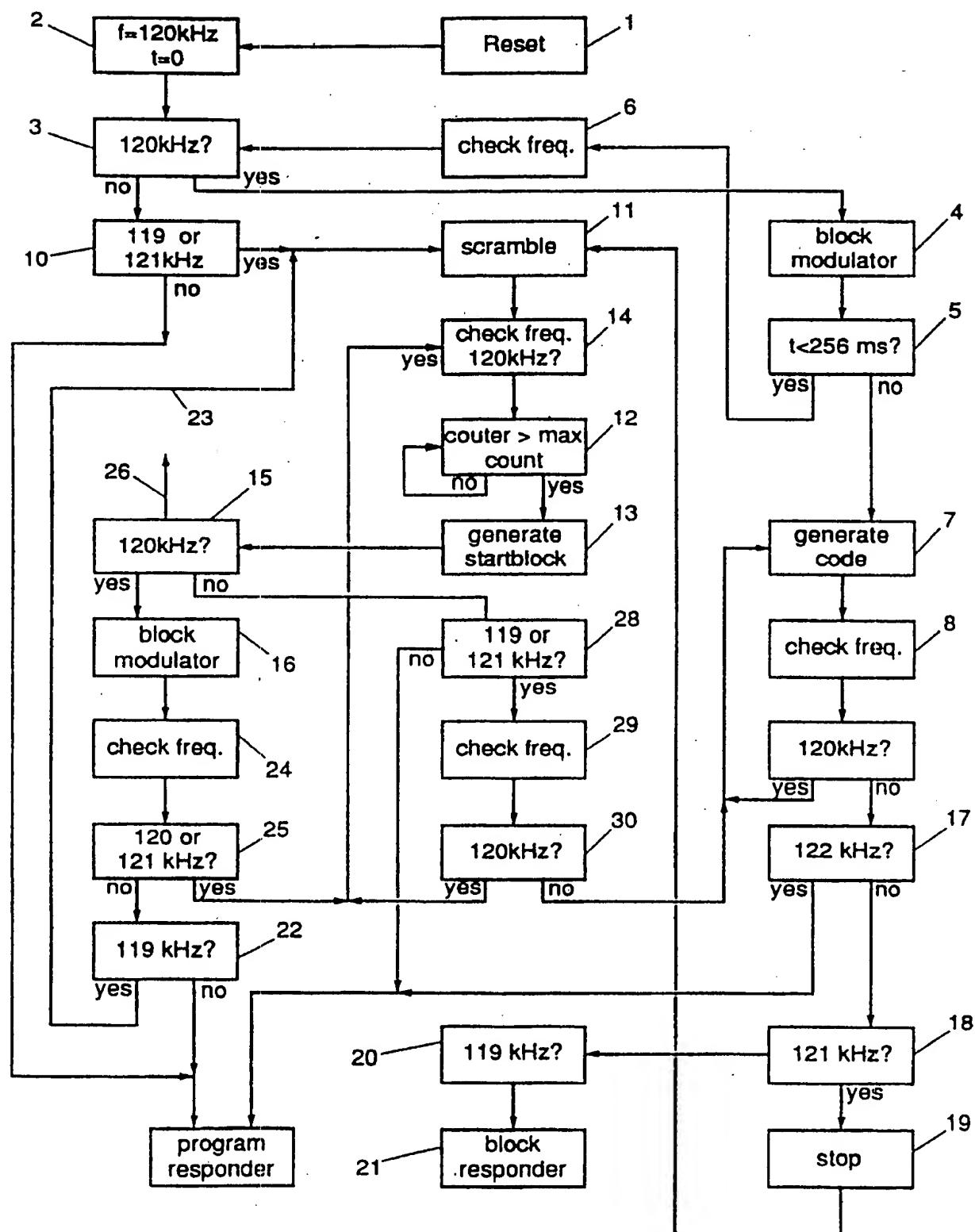


FIG. 1

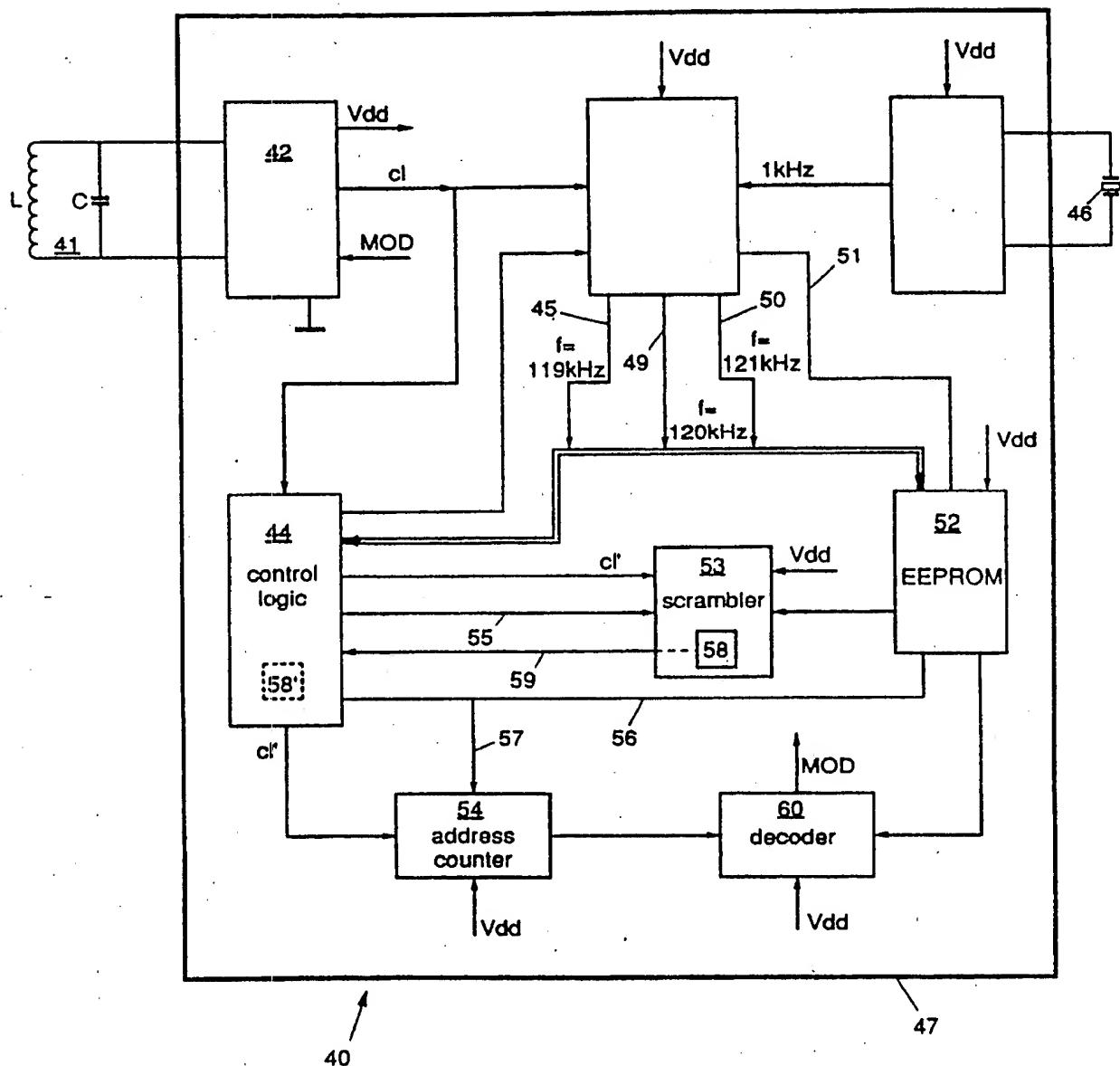


FIG. 2